## CLAIMS

What is claimed and desired to be secured by Letters Patent is:

1. A system for generating a first clock frequency for a plurality of data bursts compressed in time, the system comprising:

a transmitter for transmitting a composite stream using the data bursts clocked at a second clock frequency; and

a receiver for acquiring said composite stream and generating the first clock frequency.

2. The system of claim 1, wherein said second clock frequency is higher than the first clock frequency.

3. The system of claim 1, wherein said receiver includes a de-multiplexer for outputting the data bursts at the first clock frequency.

4. The system of claim 3, wherein said de-multiplexer includes a FIFO circuit.

5. The system of claim 3, wherein said de-multiplexer includes a phase locked loop adapted to generate the first clock frequency using said second clock frequency.

6. The system of claim 5, wherein said digital phase locked loop includes a second order feedback loop.

7. The system of claim 6, wherein said second order feedback loop includes a half period calculator circuit.

8. A system for generating a first clock frequency for a plurality of digital data sub-blocks compressed in time, said system comprising:

a multiplexer for multiplexing each of the plurality of digital data sub-blocks into a plurality of data blocks of higher speed digital data;

a receiver for acquiring the width in data elements of a digital data sub-block and the width in data elements of a data block of higher speed digital data; and

a phase locked loop for computing the width of one period of a clock pulse at the first clock frequency, and generating a clock pulse at the original clock frequency.

9. A system for generating digital data bursts at a first clock rate, the system comprising:

means for acquiring a composite stream;

means for determining the audio pixels per line in said acquired composite stream; and

means for determining the first clock rate.

10. The system of claim 9, further including a means for forming said composite stream.

11. The system of claim 10, wherein said forming means includes a transmitter.

12. A method of generating data bursts at a first clock rate, said method comprising the steps of:

forming a composite stream of audio and video data;

acquiring said composite stream; and

determining the first clock rate for the data bursts from said composite stream.

13. The method of claim 12, including transmitting said composite stream at a second clock rate.

14. The method of claim 13, wherein said second clock rate is greater than the first clock rate.

15. The method of claim 12, including computing the average number of audio pixels per line.

16. The method of claim 12, wherein determining the first clock includes using a total line width and number of audio pixels in each line.

17. The method of claim 12, including inserting audio data into video data forming said composite stream.

18. The method of claim 12, including acquiring a width in data elements of a digital data burst.

19. The method of claim 12, including acquiring a width in data elements of a data block of higher speed digital data.

20. The method of claim 12, including computing a width of one period of a clock pulse at the first clock frequency.

21. A method of generating a first clock frequency for a plurality of digital data bursts, compressed in time, where each of the plurality of digital data bursts has been multiplexed into one of a plurality of data blocks of higher speed digital data, said method comprising the steps of:

acquiring the width in data elements of a digital data burst;

acquiring the width in data elements of a data block of higher speed digital data;

computing the width of one period of a clock pulse at the first clock frequency; and

generating a clock pulse at the first clock frequency.

22. A method of generating a first clock frequency for a plurality of digital data sub-blocks compressed in time, where each of the plurality of digital data sub-blocks has been multiplexed into one of a plurality of data blocks of higher speed digital data, comprising the steps of:

acquiring the width in data elements of a digital data sub-block;

acquiring the width in data elements of a data block of higher speed digital data;

computing the width of one period of a clock pulse at the first clock frequency; and

generating a clock pulse at the first clock frequency.

23. A method of generating a first clock frequency for a burst of digital data compressed in time, wherein the burst of digital data has been multiplexed into a block of higher speed digital data, comprising the steps of:

acquiring the width in data elements of the burst of digital data;

acquiring the width in data elements of the block of higher speed digital data;

computing the width of one period of a clock pulse at the first clock frequency; and

generating a clock pulse at the first clock frequency.

24. A method of generating a first clock frequency for a sub-block of digital data compressed in time, which has been multiplexed into a block of higher speed digital data, comprising the steps of:

acquiring the width in data elements of the sub-block of digital data;

acquiring the width in data elements of the block of higher speed digital data;

computing the width of one period of a clock pulse at the first clock frequency; and

generating a clock pulse at the first clock frequency.